

AAX_BigEndianNativeSwapSequence
InPlace

AAX_LittleEndianNativeSwap
SequenceInPlace

AAX_EndianSwapSequenceIn
Place

```
graph LR; A[AAX_BigEndianNativeSwapSequenceInPlace] --> C[AAX_EndianSwapSequenceInPlace]; B[AAX_LittleEndianNativeSwapSequenceInPlace] --> C;
```

The diagram illustrates the relationship between three AAX swap sequence functions. On the left, two white boxes represent the base functions: `AAX_BigEndianNativeSwapSequenceInPlace` (top) and `AAX_LittleEndianNativeSwapSequenceInPlace` (bottom). On the right, a gray box represents the derived function: `AAX_EndianSwapSequenceInPlace`. Two blue arrows point from the left boxes to the right box, indicating that both the big-endian and little-endian native swap functions inherit from or implement the generic `AAX_EndianSwapSequenceInPlace` function.